EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	310	simulation near accelerat$3	US-PGPUB; USPAT	OR	ON	2007/09/04 08:38
S7	1	fuji-mototsugu$.in.	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:13
S9	36	tada-osamu$.in.	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:14
S11	6	morimoto-kazunobu$.in.	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:14
S12	69	yamagiwa-akira$.in.	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:14
S60	0	nanao-hisashi$.in. and @pd>"20060706"	US-PGPUB; USPAT	OR	ON	2007/09/04 10:15
S75	146	703/14.ccls. and @pd>"20060706"	US-PGPUB; USPAT	OR	ON	2007/09/04 10:18
S76	27	simulation with emulation with parallel	US-PGPUB; USPAT	OR	ON	2007/08/30 11:40
S77	11	simulation with emulation with simultaneous$2	US-PGPUB; USPAT	OR	ON	2007/08/30 11:43
S78	9	("5572710"|"5838948"|"5872953"|"6009256"|"6028993"|"6134516"|"6148436"|"6182258"|"6202044").PN.	US-PGPUB; USPAT	OR	ON	2007/08/30 11:43
S79	9	("5572710"|"5838948"|"5872953"|"6009256"|"6028993"|"6134516"|"6148436"|"6182258"|"6202044").PN.	US-PGPUB; USPAT	OR	ON	2007/09/04 08:38
S80	36	(logic near2 simulat$3 near2 accelerat$3) and FPGA	US-PGPUB; USPAT	OR	ON	2007/09/04 09:11
S81	32	S80 and (bridge or interfac$3)	US-PGPUB; USPAT	OR	ON	2007/09/04 09:12
S82	7	S81 and (direction near2 signal$2)	US-PGPUB; USPAT	OR	ON	2007/09/04 09:27
S83	0	(simulat$3 with emulat43 with (parallel or simultaneous$2))	US-PGPUB; USPAT	OR	ON	2007/09/04 09:28
S84	0	(simulat$3 with emulat43 with simultaneous$2)	US-PGPUB; USPAT	OR	ON	2007/09/04 09:28
S85	0	(verificat$3 with emulat43 with simultaneous$2)	US-PGPUB; USPAT	OR	ON	2007/09/04 09:28
S86	2958	(logic or simulat$3) near3 accelerat$3	US-PGPUB; USPAT	OR	ON	2007/09/04 09:29

S87	1982	S86 and (simultaneous$2 or parallel)	US-PGPUB; USPAT	OR	ON	2007/09/04 09:30
S88	226	S87 and FPGA	US-PGPUB; USPAT	OR	ON	2007/09/04 09:30
S89	3	S88 and ((bi?directional or two?way) adj signal?)	US-PGPUB; USPAT	OR	ON	2007/09/04 09:35
S90	1876	(direction adj control adj signal)	US-PGPUB; USPAT	OR	ON	2007/09/04 09:57
S91	39	S90 and FPGA	US-PGPUB; USPAT	OR	ON	2007/09/04 09:35
S92	12	S91 and simulat$3	US-PGPUB; USPAT	OR	ON	2007/09/04 09:35
S93	5558	(direction with control with signal) and (bi?directional or two?way)	US-PGPUB; USPAT	OR	ON	2007/09/04 09:58
S94	180	S93 and FPGA	US-PGPUB; USPAT	OR	ON	2007/09/04 09:58
S95	180	S93 and FPGA$2	US-PGPUB; USPAT	OR	ON	2007/09/04 09:58
S96	74	S95 and connector	US-PGPUB; USPAT	OR	ON	2007/09/04 09:58
S97	9	S96 and simulat$3	US-PGPUB; USPAT	OR	ON	2007/09/04 09:58
S98	433	renesas adj technology$.as. and @pd>"20070308"	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:12
S99	2	S98 and fpga	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:16
S100	0	S98 and (simulation with accelerat$3)	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:13
S101	0	fuji-mototsugu$.in. and @pd>"20070308"	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:13
S102	0	tada-osamu$.in. and @pd>"20070308"	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:14
S103	0	morimoto-kazunobu$.in. and @pd>"20070308"	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:14
S104	0	yamagiwa-akira$.in. and @pd>"20070308"	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:14
S105	0	nanao-hisashi$.in. and @pd>"20070308"	US-PGPUB; USPAT	OR	ON	2007/09/04 10:15

S10 6	44	S98 and ((bridge or interface) adj circuit)	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:16
S10 7	4	S106 and simulat$3	US-PGPUB; USPAT; EPO	OR	ON	2007/09/04 10:16
S10 8	103	703/14.ccls. and @pd>"20070308"	US-PGPUB; USPAT	OR	ON	2007/09/04 10:19
S10 9	9	S108 and (accelerat$3 with simulat$3)	US-PGPUB; USPAT	OR	ON	2007/09/04 10:19
S11 0	5	S109 and fpga	US-PGPUB; USPAT	OR	ON	2007/09/04 10:21
S11 1	0.	S110 and (direction with control)	US-PGPUB; USPAT	OR	ON	2007/09/04 10:21
S11 2	2	S110 and (direction with signal)	US-PGPUB; USPAT	OR	ON	2007/09/04 10:21
S11 3	0	"08733352".pran.	US-PGPUB; USPAT	OR	ON	2007/09/04 12:42
S11 4	0	"08733352".prn.	US-PGPUB; USPAT	OR	ON	2007/09/04 12:43
S11 5	1	"5841967".pn.	US-PGPUB; USPAT	OR	ON	2007/09/04 12:43